



FORM PTO-1449

FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1778.0110001APPLICATION NO.
09/223,046APPLICANT
van Hook *et al.*FILING DATE
December 30, 1998GROUP
2154

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1	4,511,990	04/1985	Hagiwara <i>et al.</i>			
	AB1	4,879,676	11/1989	Hansen			
	AC1	5,392,228	02/1995	Burgess <i>et al.</i>			
	AD1	5,517,438	05/1996	Dao-Trong <i>et al.</i>			
	AE1	5,550,768	08/1996	Ogilvie <i>et al.</i>			
	AF1	5,726,927	03/1998	Wolrich <i>et al.</i>			
	AG1	5,729,724	03/1998	Sharangpani <i>et al.</i>			
	AH1	5,774,709	06/1998	Worrell			
	AI1	5,790,827	08/1998	Leung			
	AJ1	5,809,294	09/1998	Ando			
	AK1	5,815,695	09/1998	James <i>et al.</i>			

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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1	0 681 236 A1	11/1995	Europe	G06F	9/302	Yes No
	AM1	0 681 236 B1	11/1995	Europe	G06F	9/302	Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

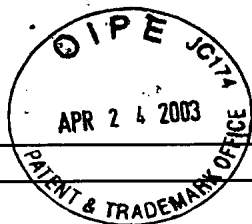
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	1	Bier, J., "DSP16xxx Targets Communications Apps," <i>Microprocessor Report</i> , MicroDesign Resources, September 15, 1997, pages 11-15.
	AS	1	Gwennap, L., "UltraSparc Adds Multimedia Instructions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 8, No. 16, December 5, 1994, pages 1-3.
	AT	1	Gwennap, L., "Intel's MMX Speeds Multimedia," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 3, March 5, 1996, pages 1-6.

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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



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	AA2	5,848,269	12/1998	Hara			
	AB2	5,852,726	12/1998	Lin <i>et al.</i>			
	AC2	5,867,682	02/1999	Witt <i>et al.</i>			
	AD2	5,880,984	03/1999	Burchfiel <i>et al.</i>			
	AE2	5,887,183	03/1999	Agarwal <i>et al.</i>			
	AF2	5,933,650	08/1999	van Hook <i>et al.</i>			
	AG2	5,953,241	09/1999	Hansen <i>et al.</i>			
	AH2	6,065,115	05/2000	Sharangpani <i>et al.</i>			
	AI2	5,793,661	08/1998	Dulong <i>et al.</i>			
	AJ2						
	AK2						

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
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	AM2						Yes No
	AN2						Yes No
	AO2						Yes No
	AP2						Yes No

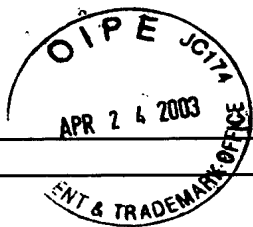
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	2	Heinrich, J., <i>MIPS R4000 Microprocessor User's Manual</i> , Second Edition, MIPS Technologies, Inc., 1994, pages 154-155, 157, 159, 161, 168, 170-171, B-9-10, B-13, B-17, B-19, B-21, B-23, B-27, B-38, B-40, and B-62.
	AS	2	IEEE Standard for Binary Floating-Point Arithmetic, IEEE, 1985, pages 1-14.
	AT	2	Killian, E., "MIPS Extensions for Digital Media," Microprocessor Forum, October 22-23, 1996, pages 1-5.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA3				RECEIVED APR 28 2003 Technology Center 2100		
	AB3						
	AC3						
	AD3						
	AE3						
	AF3						
	AG3						
	AH3						
	AI3						
	AJ3						
	AK3						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL3						Yes No
	AM3						Yes No
	AN3						Yes No
	AO3						Yes No
	AP3						Yes No

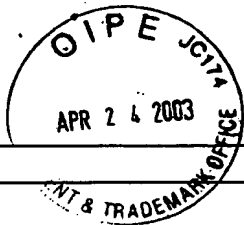
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	<u>3</u>	"88410 Second Level Cache," <i>Microprocessor Forum</i> , Motorola Inc., November 1991, page L 00622-L 00641.
	AS	<u>3</u>	TMS32010 User's Guide, Texas Instruments Incorporated, 1983, page 1183.
	AT	<u>3</u>	Turley, J., "Siemens TriCore Revives CISC Techniques," <i>Microprocessor Report</i> , MicroDesign Resources, November 17, 1997, pages 13-16.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4						
	AB4						
	AC4						
	AD4						
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	AF4						
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No
	AP4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AR	<u>4</u>	VIS™ Instruction Set User's Manual, Sun Microsystems, Inc., 1997, pages 1-136, http://www.nondot.org/sabre/os/files/processors/VISInstructionSetUsersManual.pdf .
	AS	<u>4</u>	
	AT	<u>4</u>	

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08/27/03

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FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
0056.10USAPPLICATION NO.
09/223,046APPLICANT
van Hook *et al.*FILING DATE
December 30, 1998GROUP
2154

U.S. PATENT DOCUMENTS

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	AA1						
	AB1						
	AC1						
	AD1						
	AE1						
	AF1						
	AG1						
	AH1						
	AI1						
	AJ1						
	AK1						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

96	AR	1	Plaintiff MIPS Technologies' Objections and Responses to First Set of Interrogatories of Defendant Lexra, Inc., 38 Pages with Exhibits A-C, Filed February 28, 2000 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AS	1	
	AT	1	

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